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## Simple Norton equivalents

The problem contains a circuit with constant voltage source that makes the associated MNA matrix non-symmetric. Part (a) looks like it is there to provide a hint that this source $V_{s}$ and its internal resistance $R_{s}$ can likely be replaced by a constant current source.
Here two voltage source configurations will be compared to a current source configuration, with the assumption that equivalent circuit configurations can be found.

First voltage source configuration First consider the source and internal series resistance configuration sketched in fig. 1.1, with a purely resistive load.


Figure 1.1: First voltage source configuration
The nodal equations for this system are

1. $-i_{L}+\left(V_{1}-V_{L}\right) Z_{s}=0$
2. $V_{L} Z_{L}+\left(V_{L}-V_{1}\right) Z_{s}=0$
3. $V_{1}=V_{s}$

In matrix form these are

$$
\left[\begin{array}{ccc}
Z_{S} & -Z_{S} & -1  \tag{1.1}\\
-Z_{S} & Z_{S}+Z_{L} & 0 \\
1 & 0 & 0
\end{array}\right]\left[\begin{array}{c}
V_{1} \\
V_{L} \\
i_{L}
\end{array}\right]=\left[\begin{array}{c}
0 \\
0 \\
V_{s}
\end{array}\right]
$$

This has solution

$$
\begin{gather*}
V_{L}=V_{s} \frac{R_{L}}{R_{L}+R_{s}}  \tag{1.2a}\\
i_{L}=\frac{V_{s}}{R_{L}+R_{s}}  \tag{1.2b}\\
V_{1}=V_{s} . \tag{1.2c}
\end{gather*}
$$

Second voltage source configuration Now consider the same voltage source, but with the series resistance location flipped as sketched in fig. 1.2.


Figure 1.2: Second voltage source configuration
The nodal equations are

1. $V_{1} Z_{S}+i_{L}=0$
2. $-i_{L}+V_{L} Z_{L}=0$
3. $V_{L}-V_{1}=V_{S}$

These have matrix form

$$
\left[\begin{array}{ccc}
Z_{s} & 0 & 1  \tag{1.3}\\
0 & Z_{L} & -1 \\
-1 & 1 & 0
\end{array}\right]\left[\begin{array}{l}
V_{1} \\
V_{L} \\
i_{L}
\end{array}\right]=\left[\begin{array}{c}
0 \\
0 \\
V_{s}
\end{array}\right]
$$

This configuration has solution

$$
\begin{gather*}
V_{L}=V_{s} \frac{R_{L}}{R_{L}+R_{s}}  \tag{1.4a}\\
i_{L}=\frac{V_{s}}{R_{L}+R_{s}}  \tag{1.4b}\\
V_{1}=-V_{s} \frac{R_{s}}{R_{L}+R_{s}} \tag{1.4c}
\end{gather*}
$$

Observe that the voltage at the load node and the current through this impedance is the same in both circuit configurations. The internal node voltage is different in each case, but that has no measurable effect on the external load.

Current configuration Now consider a current source and internal parallel resistance as sketched in fig. 1.3.


Figure 1.3: Current source configuration
There is only one nodal equation for this circuit

1. $-I_{S}+V_{L} Z_{S}+V_{L} Z_{L}=0$

The load node voltage and current follows immediately

$$
\begin{gather*}
V_{L}=\frac{I_{S}}{Z_{L}+Z_{S}}  \tag{1.5a}\\
i_{L}=V_{L} Z_{L}=\frac{Z_{L} I_{S}}{Z_{L}+Z_{S}} \tag{1.5b}
\end{gather*}
$$

The goal is to find a value for $I_{L}$ so that the voltage and currents at the load node match either of the first two voltage source configurations. It has been assumed that the desired parallel source
resistance is the same as the series resistance in the voltage configurations. That was just a guess, but it ends up working out.

From eq. (1.5a) and eq. (1.2a) that equivalent current source can be found from

$$
\begin{equation*}
V_{L}=V_{s} \frac{R_{L}}{R_{L}+R_{s}}=\frac{I_{s}}{Z_{L}+Z_{s}} \tag{1.6}
\end{equation*}
$$

or

$$
\begin{align*}
I_{s}= & V_{s} \frac{R_{L}\left(Z_{L}+Z_{s}\right)}{R_{L}+R_{s}} \\
= & \frac{V_{s}}{R_{S}} \frac{R_{s} R_{L}\left(Z_{L}+Z_{s}\right)}{R_{L}+R_{s}} \\
& I_{s}=\frac{V_{s}}{R_{S}} . \tag{1.8}
\end{align*}
$$

The load is expected to be the same through the load, and is

$$
\begin{equation*}
i_{L}=V_{L} Z_{L}==V_{s} \frac{R_{L} Z_{L}}{R_{L}+R_{s}}=\frac{V_{s}}{R_{L}+R_{s}} \tag{1.9}
\end{equation*}
$$

which matches eq. (1.2b).
Remarks The equivalence of the series voltage source configurations with the parallel current source configuration has been demonstrated with a resistive load. This is a special case of the more general Norton's theorem, as detailed in [2] and [1] §5.1. Neither of those references prove the theorem. Norton's theorem allows the equivalent current and resistance to be calculated without actually solving the system. Using that method, the parallel resistance equivalent follows by summing all the resistances in the source circuit with all the voltage sources shorted. Shorting the voltage sources in this source circuit results in the same configuration. It was seen directly in the two voltage source configurations that it did not matter, from the point of view of the external load, which sequence the internal series resistance and the voltage source were placed in did not matter. That becomes obvious with knowledge of Norton's theorem, since shorting the voltage sources leaves just the single resistor in both cases.

## Bibliography

[1] J.D. Irwin. Basic Engineering Circuit Analysis. MacMillian, 1993.
[2] Wikipedia. Norton's theorem - wikipedia, the free encyclopedia, 2014. URL http://en. wikipedia.org/w/index.php?title=Norton\'s_theorem\&oldid=629143825. [Online; accessed 1-November-2014].

